

## CERTIFICATE OF PARTICIPATION

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No. 07/30.06.2021

This certificate of participation has been granted to Mr. **Heorhii Vorobets**, Ph.D., Associate Professor, Head of Computer Systems and Networks Department of Yuriy Fedkovych Chernivtsi National University (Ukraine), for his participation, between 20.05.2021 and 30.06.2021, in the advanced training and research course on *Design of mobile and embedded microprocessor devices based on Xilinx and Intel (Altera) FPGA for automation, coding and information protection in computer systems of the telemetry, telecontrol and data transmission, comprising 180 hours (6 credits), conducted at the Ştefan cel Mare University of Suceava (Romania), Faculty of Electrical Engineering and Computer Science.* 

Prof. Valentin POPA, Philipsucary

Dean,

Prof. Laurențiu-Dan MILICI, Ph.D

June 2021